

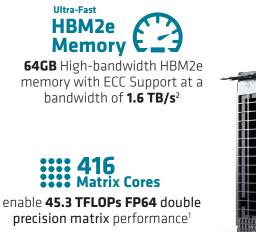
Bringing Exascale-Class Technologies to Mainstream HPC and AI



AMD Instinct[™] MI210 leverages the same technology powering the **world's most powerful supercomputers**



FP16 and BF16 performancefor AI training and machine learning







technology enables up to **300 GB/s** (Dual) and **600 GB/s** (Quad) of total aggregate peak P2P theoretical I/O bandwidth in GPU hives³



AMD CDNA[™] 2 Architecture

Powered by the 2nd Generation AMD CDNA[™] architecture, the MI210 accelerator is purpose-built for HPC and AI, and delivers outstanding performance over competitive data center PCle[®] GPUs today.¹

AMD Infinity Fabric[™] Technology

3rd Gen AMD Infinity Fabric maximizes system efficiency throughout by enabling high speed direct GPU to GPU connectivity.



AMD ROCm[™] Ecosystem

With the ROCm open software platform, HPC and AI developers can now access a range of open compute languages, compilers, libraries and tools for production-class deployments.

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alculations conducted by AMD Performance Labs as of Jan 14, 2022, for the AMD Instinct⁺ M/210 (64GB HBM2e PCIe[®] card) accelerator at 1/200 MHz peak boost engine clock resulted in 45.3 TFLOP5 peak theoretical double precision (PF64 Matrix), 22.6 TFLOP5 peak theoretical double precision (FF64), and 18.0 TFLOP5 ket theoretical and precision (FF64) and 18.0 TFLOP5 peak theoretical double precision (FF64), theoretical floating-point performance. Published resulted in 45.4 TFLOP5 peak theoretical and precision (FF64), and 18.0 TFLOP5 peak theoretical double precision (FF64), theoretical floating-point performance. Published resulted in 15.4 TFLOP5 peak theoretical double precision (FF64), theoretical floating-point performance. Published resulted in 15.4 TFLOP5 peak theoretical and precision (FF64), theoretical floating-point performance. Published resulted in 0.4 TFLOP5 peak theoretical floating-point performance. Published resulted in 0.4 AII and theoretical floating-precision (FF64), and 15.0 TFLOP5 peak theoretical floating-point performance. Thus, J/www.rndia.com/content/dam/en-z/J/soliton5(3) TFLOP5 peak theoretical floating-point performance. Thus, J/www.rndia.com/content/dam/en-z/J/soliton5(3) TFLOP5 peak theoretical floating-precision (FF64). Theoretical floating-point performance and the site of 200 Sp14 of 201 Se14 MBXD 201 Se14 MHXD 201 Se14